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FACSIMILE COVER SHEET

TO: Examiner Chuong A. LUU – United States Patent and Trademark Office; Art Unit 2818

CLIENT NAME/NUMBER: 54484

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FROM: Douglas J. Visnius

DATE: December 30, 2005

NUMBER OF PAGES (INCLUDING COVER SHEET): 6

COMMENTS/INSTRUCTIONS:

Please see attached Response to Examiner's Office Action of October 5, 2005 for U.S. Patent Application Serial No. 10/714,440.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
LENOBLE

Serial No. 10/714,440

Confirmation No. 9369

Filing Date: NOVEMBER 14, 2003

For: PROCESS FOR FABRICATING A
SHORT-GATE-LENGTH MOS
TRANSISTOR AND INTEGRATED
CIRCUIT COMPRISING SUCH A
TRANSISTOR

) Examiner: C. LUU

) Art Unit: 2818

) Attorney Docket No.
02GR126654484

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RESPONSE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Official Action of October 5, 2005,
please enter the remarks set out below.

In re Patent Application of:

LENOBLE

Serial No. 10/714,440

Filing Date: NOVEMBER 14, 2003

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